Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A method comprising:

providing at least two wireless transceiver interfaces; and

deriving device characteristics and priority information from a priority and a type of each said wireless transceiver interface;

sending said device characteristics and priority information to each said wireless transceiver interface; and

disabling one wireless transceiver interface while another wireless transceiver interface is conducting communication.

Claim 2 (currently amended): The method of claim 1, including:

detecting activity signals from said at least two wireless transceiver interfaces;

assigning a the priority to each said wireless transceiver interface;

tracking a potential communication associated with each said wireless transceiver interface;

arbitrating control of communication between said at least two wireless transceiver interfaces based on the priority and the potential communication; and

selectively energizing each said wireless transceiver interface based on the control of communication.

Claim 3 (cancel)

Claim 4 (currently amended): The method of claim [[3]] 2, wherein assigning said priority including prioritizing each said wireless transceiver interface based on a first criterion indicative of an overhead associated with said potential communication for each said wireless transceiver interface.

Claim 5 (currently amended): The method of claim [[3]] 2, wherein assigning said priority including prioritizing each said wireless transceiver interface based on a second criterion indicative of an amount of data associated with said potential communication for each said wireless transceiver interface.

Claim 6 (currently amended): The method of claim [[3]] 2, wherein assigning said priority including prioritizing each said wireless transceiver interface based on a third criterion indicative of a power consumption associated with said potential communication for each said wireless transceiver interface.

Claim 7 (currently amended): The method of claim [[3]] 2, including:

querying to acquire a channel lock for the control of communication; and

providing ownership of the channel lock to one of the at least two wireless transceiver interfaces based on the device characteristics and priority information.

Claim 8 (original):   The method of claim 7, including:

in response to an indication, gaining ownership of the channel lock; and

opening a communication channel for a communication session associated with said one of the at least two active wireless transceiver interfaces.

Claim 9 (original):   The method of claim 8, including releasing the ownership of the channel lock when the communication session is finished.

Claim 10 (original):   The method of claim 9, including transferring the ownership of the channel lock to another one of the at least two active wireless transceiver interfaces when said communication channel becomes available for another communication session through time slicing.

Claim 11 (currently amended):   An apparatus comprising:

an antenna;

a first communication interface coupled to the antenna corresponding to a first wireless device;

a second communication interface <u>coupled to the antenna</u> corresponding to a second wireless device; and

a module operably coupled to the first and second communication interfaces to disable communication between the first communication interface and said first wireless device while the second communication interface is conducting communication for said second wireless device.

Claim 12 (currently amended):   The apparatus of claim 11, wherein said first communication interface to provide a first activity signal, said second communication interface to provide a second activity signal, and said module to:

detect the first and second activity signals, assign a priority to each said active wireless device, track a potential communication associated with each said communication interface, and to arbitrate control of communication between the first and second communication interfaces based on the priority and the potential communication corresponding to said first and second wireless devices; and

selectively energize the first and second communication interfaces based on the <u>control of</u> communication ~~protocol to mitigate cross-interference between said first and second wireless devices~~.

Claim 13 (currently amended):   The apparatus of claim [[12]] <u>11</u>, wherein said module to:

determine ~~the~~ <u>a</u> type of <u>and assign a priority to</u> each said wireless device ~~to mitigate cross-interference between said first and second wireless devices~~;

derive device characteristics and priority information from the priority and the type of each said wireless device; and

send said device characteristics and priority information to each said communication interface.

Claim 14 (original):   The apparatus of claim 13, wherein each said communication interface to:

query said module to acquire a channel lock for the control of communication;

in response to an indication from said module, gain ownership of the channel lock;

open a communication channel for a communication session; and

release the ownership of the channel lock when the communication session is finished.

Claim 15 (currently amended):   The apparatus of claim 14, wherein said module to:

provide ownership of the channel lock to one of the first and second wireless devices based on the ~~device~~ type and priority information; and

transfer the ownership of the channel lock to another one of the first and second wireless devices when said communication channel becomes available for another communication session through time slicing.

Claim 16 (currently amended):   An article comprising a medium storing instructions that enable a processor-based system to:

provide at least two wireless transceiver interfaces; ~~and~~

<u>derive device characteristics and priority information from a priority and a type of each said wireless transceiver interface;</u>

<u>send said device characteristics and priority information to each said wireless transceiver interface; and</u>

disable one wireless transceiver interface while another wireless transceiver interface is conducting communication.

Claim 17 (original):   The article of claim [[15]] <u>16</u> further storing instructions that enable the processor-based system to:

detect activity signals from said at least two wireless transceiver interfaces;

assign [[a]] <u>the</u> priority to each said wireless transceiver interface;

track a potential communication associated with each said wireless transceiver interface;

arbitrate control of communication between said at least two wireless transceiver interfaces based on the priority and the potential communication; and

selectively energize each said wireless transceiver interface based on the control of communication.

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Claim 18 (cancel)

Claim 19 (original):   The article of claim [[15]] 17 further storing instructions that enable the processor-based system to:

query to acquire a channel lock for the control of communication; and

provide ownership of the channel lock to one of the at least two wireless transceiver interfaces based on the device characteristics and priority information.

Claim 20 (cancel)

Claim 21 (currently amended):   A processor-based system comprising:

a processor;

a storage operably coupled to said processor to store a priority protocol capable of tracking to track pending transactions associated with at least two active wireless transceivers and prioritize prioritizing one of said at least two active wireless transceivers;

at least two wireless transceiver interface devices operably coupled to said processor to provide corresponding gating signals associated with the at least two active wireless transceivers; and

an arbitration device operably coupled to said at least two wireless transceiver interface devices to selectively provide communication control to said one of at least two active wireless transceivers based on the priority protocol.

Claim 22 (currently amended):   The processor-based system of claim 21, wherein said arbitration device to selectively powers power up or down the at least two wireless transceiver interface devices based on the communication control to mitigate cross-interference between said at least two active wireless transceivers.

Claim 23 (original):   The processor-based system of claim 22, wherein said arbitration device to:

determine the type of each said active wireless transceiver;

derive device characteristics and priority information from the priority and the type of each said active wireless transceiver; and

send said device characteristics and priority information to each said active wireless transceiver.

Claim 24 (original): The processor-based system of claim 23, wherein each said wireless transceiver interface device to:

query said arbitration device to acquire a channel lock for the communication control;

in response to an indication from said arbitration device, gain ownership of the channel lock;

open a communication channel for a communication session; and

release the ownership of the channel lock when the communication session is finished.

Claim 25 (original): The processor-based system of claim 24, wherein said arbitration device to:

provide ownership of the channel lock to one of the at least two active wireless transceivers based on the device characteristics and priority information; and

transfer the ownership of the channel lock to another one of the at least two active wireless transceivers when said communication channel becomes available for another communication session through time slicing.

Claim 26 (currently amended): A personal computer system comprising:

a processor;

a storage operably coupled to said processor to store a priority protocol ~~capable of tracking~~ to track pending transactions associated with at least two active wireless transceivers and ~~prioritizing~~ prioritize one of said at least two active wireless transceivers; and

a shared interface to operably couple a chipset with a radio device interface including:

at least two wireless transceiver interface devices operably coupled to said processor to provide corresponding gating signals associated with the at least two active wireless transceivers, and

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an arbitration device operably coupled to said at least two wireless transceiver interface devices to selectively provide communication control to said one of at least two active wireless transceivers based on the priority protocol.

Claim 27 (currently amended): The personal computer system of claim 26, wherein said arbitration device to:

selectively power up or down the at least two wireless transceiver interface devices based on the communication control ~~to mitigate cross-interference between said at least two active wireless transceivers~~;

determine the type of each said active wireless transceiver;

derive device characteristics and priority information from the priority and the type of each said active wireless transceiver; and

send said device characteristics and priority information to each said active wireless transceiver.

Claim 28 (original): The personal computer system of claim 27, wherein one of said at least two active wireless transceivers to communicate using a short range communication standard-based protocol while another one of said at least two active wireless transceivers to communicate using a long range communication standard-based protocol ~~with respect to the short range communication standard-based protocol~~.

Claim 29 (original): The personal computer system of claim 26, wherein each said wireless transceiver interface device to:

query said arbitration device to acquire a channel lock for the communication control;

in response to an indication from said arbitration device, gain ownership of the channel lock;

open a communication channel for a communication session; and

release the ownership of the channel lock when the communication session is finished.

Claim 30 (original): The personal computer system of claim 29, wherein said arbitration device to:

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provide ownership of the channel lock to one of the at least two active wireless transceivers based on the device characteristics and priority information; and

transfer the ownership of the channel lock to another one of the at least two active wireless transceivers when said communication channel becomes available for another communication session through time slicing.

Claim 31 (new):   A personal computer system comprising:

a processor; and

at least two wireless transceivers coupled to the processor, each of the at least two wireless transceivers to provide a gating signal to indicate activity in a corresponding radio device.

Claim 32 (new):   The personal computer system of claim 31, further comprising a single antenna coupled to the at least two wireless transceivers.

Claim 33 (new):   The personal computer system of claim 31, further comprising a controller coupled to receive each of the gating signals and arbitrate a communication channel between the at least two wireless transceivers.

Claim 34 (new):   The personal computer system of claim 33, wherein the controller to arbitrate using a priority of each of the at least two wireless transceivers.